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IEEE JNL IEEE Journal or Magazine

IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IET CNF IET Conference Proceeding

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[Select All](#) [Deselect All](#)**1. Design and optimization of large size and low overhead off-chip caches**

Zhao Zhang; Zhichun Zhu; Xiaodong Zhang;  
*Computers, IEEE Transactions on*  
Volume 53, Issue 7, July 2004 Page(s):843 - 855  
Digital Object Identifier 10.1109/TC.2004.27

[AbstractPlus](#) | [References](#) | Full Text: [PDF\(1264 KB\)](#) IEEE JNL  
[Rights and Permissions](#)**2. Combining layer 2-layer 3 paging for wireless LANs**

Ozugur, T.; Sarikaya, B.;  
*Wireless Communications, IEEE Transactions on*  
Volume 5, Issue 9, September 2006 Page(s):2605 - 2614  
Digital Object Identifier 10.1109/TWC.2006.1687785

[AbstractPlus](#) | Full Text: [PDF\(668 KB\)](#) IEEE JNL  
[Rights and Permissions](#)**3. The biomechanics of lumbar graded facetectomy under anterior-shear load**

Teo, E.C.; Lee, K.K.; Qiu, T.X.; Ng, H.W.; Yang, K.;  
*Biomedical Engineering, IEEE Transactions on*  
Volume 51, Issue 3, March 2004 Page(s):443 - 449  
Digital Object Identifier 10.1109/TBME.2003.821009

[AbstractPlus](#) | [References](#) | Full Text: [PDF\(336 KB\)](#) IEEE JNL  
[Rights and Permissions](#)**4. A 65-nm Dual-Core Multithreaded Xeon® Processor With 16-MB L3 Cache**

Rusu, S.; Tam, S.; Muljono, H.; Ayers, D.; Chang, J.; Cherkauer, B.; Stinson, J.; Benoit, J.; Varada, R.; Leung, J.; Limaye, R. D.; Vora, S.;  
*Solid-State Circuits, IEEE Journal of*  
Volume 42, Issue 1, Jan. 2007 Page(s):17 - 25  
Digital Object Identifier 10.1109/JSSC.2006.885041

[AbstractPlus](#) | Full Text: [PDF\(4608 KB\)](#) IEEE JNL  
[Rights and Permissions](#)**5. A single-V/sub t/ low-leakage gated-ground cache for deep submicron**

Agarwal, A.; Hai Li; Roy, K.;  
*Solid-State Circuits, IEEE Journal of*  
Volume 38, Issue 2, Feb. 2003 Page(s):319 - 328  
Digital Object Identifier 10.1109/JSSC.2002.807414

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6. **An implementation of FTTH based home gateway supporting various services**

Wan-Ki Park; Sung-II Nam; Chang-Sic Choi; Youn-Kwae Jeong; Kwang-Roh Park;  
[Consumer Electronics, IEEE Transactions on](#)  
Volume 52, Issue 1, Feb. 2006 Page(s):110 - 115  
Digital Object Identifier 10.1109/TCE.2006.1605034

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7. **A fast high-resolution track trigger for the H1 experiment**

Naumann, J.; Baird, A.; Elsen, E.; Fleming, Y.H.; Kolander, M.; Kolya, S.; Meer, D.; Mercer, D.; Newman, P.R.; Sankey, D.; Schonning, A.; Schultz-Coulon, H.-C.; Wissing, C.;  
[Nuclear Science, IEEE Transactions on](#)  
Volume 48, Issue 4, Part 1, Aug. 2001 Page(s):1276 - 1281  
Digital Object Identifier 10.1109/23.958765

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8. **Layer 2 and 3 virtual private networks: taxonomy, technology, and standardization efforts**

Knight, P.; Lewis, C.;  
[Communications Magazine, IEEE](#)  
Volume 42, Issue 6, June 2004 Page(s):124 - 131  
Digital Object Identifier 10.1109/MCOM.2004.1304248

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9. **Finite-element analysis for lumbar interbody fusion under axial loading**

Lee, K.K.; Teo, E.C.; Fuss, F.K.; Vanneuville, V.; Qiu, T.X.; Ng, H.W.; Yang, K.; Sabitzer, R.J.;  
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Kuan-Hung Chen; Jiun-In Guo; Jinn-Shyan Wang; Ching-Wei Yeh; Jia-Wei Chen;  
[Circuits and Systems for Video Technology, IEEE Transactions on](#)  
Volume 15, Issue 5, May 2005 Page(s):704 - 715  
Digital Object Identifier 10.1109/TCSVT.2005.846441

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